



*JW*

In re Applicant:	Ramesh V. Peri et al.	§	Art Unit:	2186
Serial No.:	10/717,085	§	Examiner:	Paul W. Schlie
Filed:	November 19, 2003	§	Docket:	ITL.1059US P17918
For:	Accessing Data From Different Memory Locations in the Same Cycle	§	Assignee:	Intel Corporation

Mail Stop **Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 1**

Sir:

In response to the office action mailed December 13, 2005, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: February 9, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
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